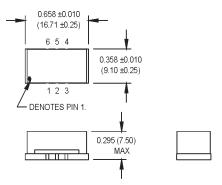
M6007 Series 9x14 mm FR-4, 3.3 Volt, HCMOS/TTL, TCVCXO

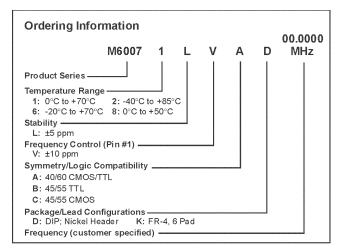




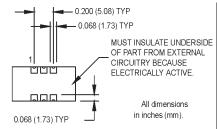


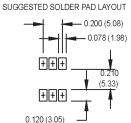
- Stratum 3 compliant stability and aging
- Ideal for WLL/DWDM/ATM, and SONET/SDH applications





M6007Sxxx - Contact factory for datasheet.





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++++	_	0.210 (5.33)
0.120 (3.05)	1	Ť
0.120 (3.03)		

Pin Connections

FUNCTION	SMT			
N/C	1			
Tristate	2			
Ground/Case	3			
Output	4			
N/C	5			
+Vdd	6			

	PARAMETER	Symbol	Min.	Тур.	Max.	Ur	nits	Condition/Notes
	Frequency Range	F	60		170	MI	Ηz	
	Operating Temperature	TA	(See Ord	(See Ordering Information)				
	Storage Temperature	Ts	-55		+105	°C		
	Frequency Stability	ÄF/F	(See Ord	(See Ordering Information)				See Note 1
	Aging							
	1st Year				1.5	рр	m	
	Thereafter (per year)				0.5	рр	m	
	Pullability/APR		(See Ord	(See Ordering Information				
၂ က	Control Voltage	Vc	0.5	1.5	2.5			
Electrical Specifications	Tuning Range				10		m/V	
	Modulation Bandwidth	fm	10			k⊦	lz	
	Input Impedance	Zin	50k			_	nms	
	Input Voltage	Vdd	3.15	3.3	3.45	٧		
	Input Current	ldd			25	m	4	
	Output Type							CMOS/TTL
	Load		2 TTL or	2 TTL or 15 pF max.				
	Symmetry (Duty Cycle)			(See Ordering Information)				
	Logic "1" Level	Voh	2.5	2.5		V		
	Logic "0" Level	Vol			0.5	٧		
	Rise/Fall Time	Tr/Tf			10	ns		
	Tristate Function		Input Log	Input Logic "1": output active				Opposite tristate
			Input Log	Input Logic "0": output disabl				logic available upon request.
	Start up Time				10	ms	5	
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	10	0 kHz	Offset from carrier
	@155.52 MHz	-60	-90	-110	-120	-12	20	dBc/Hz
Ita	Shock	MIL-S	MIL-STD-202, Method 213, C				100 g's	
Environmental	Vibration	MIL-S	MIL-STD-202, Method 204 – 204				10 g's from 10-2000 Hz	
	Thermal Cycle	MIL-S	MIL-STD-883, Method 1010, B				-55°C to +125°C, 15 minute dwell, 10 cycles	
١,٢	Hermeticity	MIL-S	MIL-STD-202, Method 112				Must meet 1 x 10 ⁻⁸	
Ľ	Max Soldering Conditions See solder profile, Figure 1							

^{1.} Stability is inclusive of five year aging at 25°C.

TTL Load – See load circuit diagram #1. HCMOS Load – See load circuit diagram #2.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.





